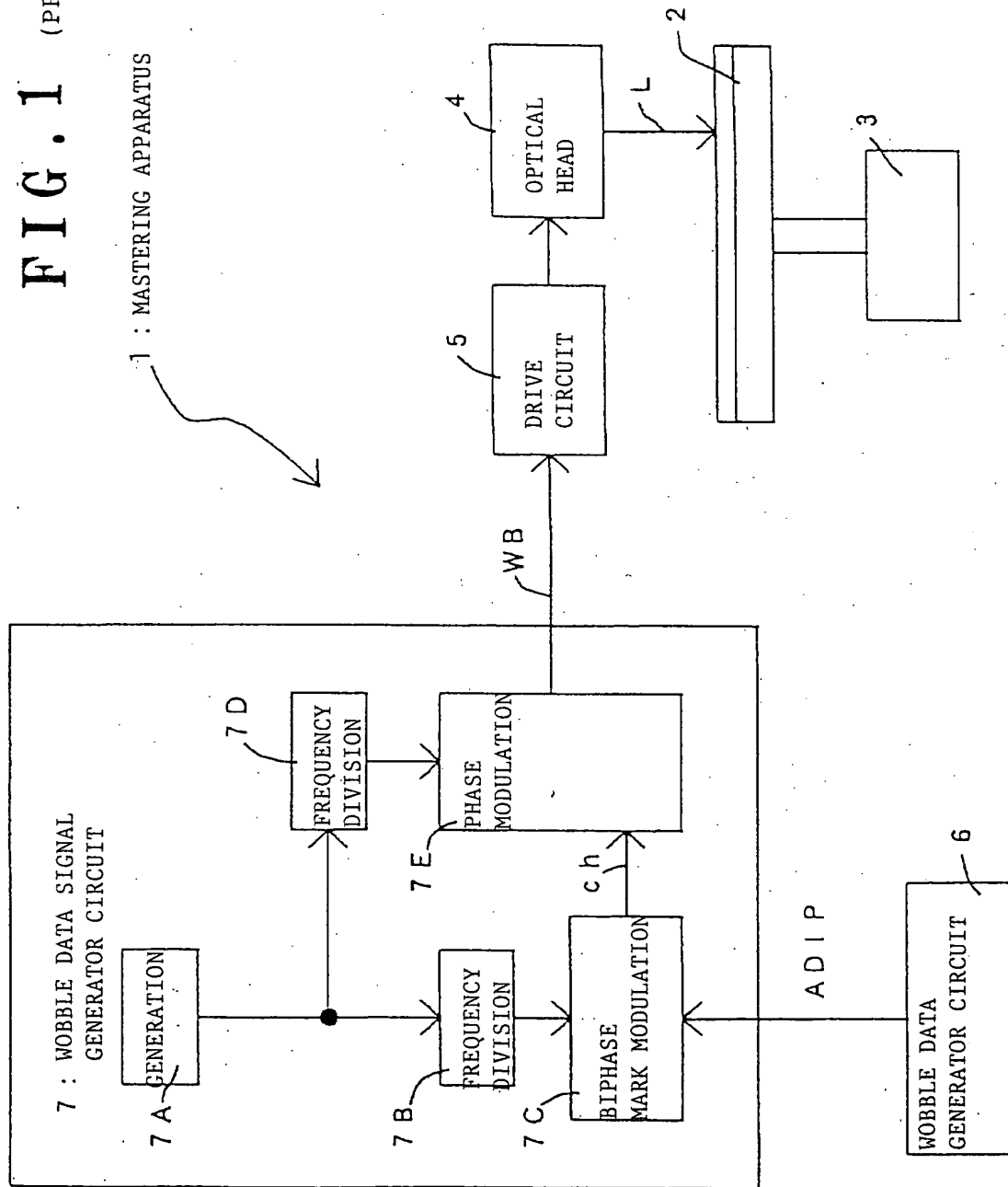


FIG. 1 (PRIOR ART)



PU01-0041
~~EP01-0041~~
~~PCN01-0041~~
~~PTW01-0041~~
~~PKR01-0041~~

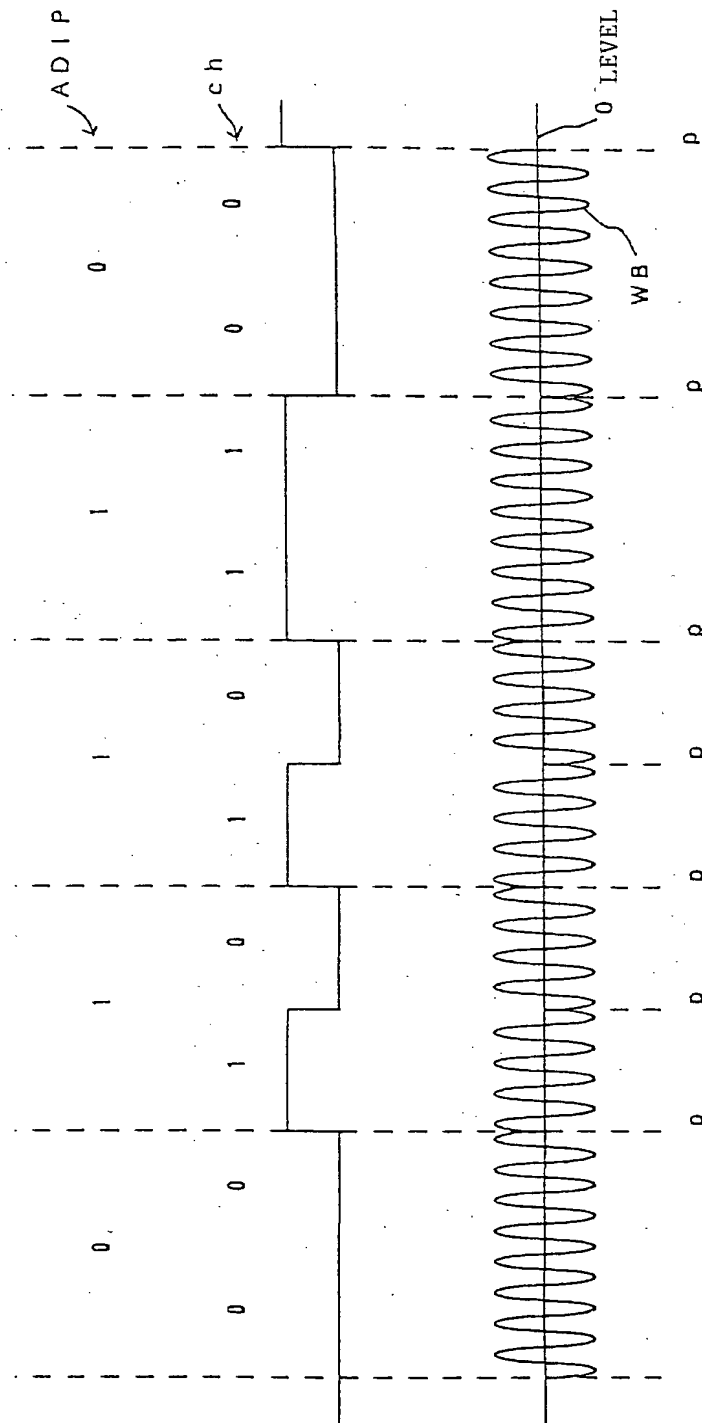


FIG. 2

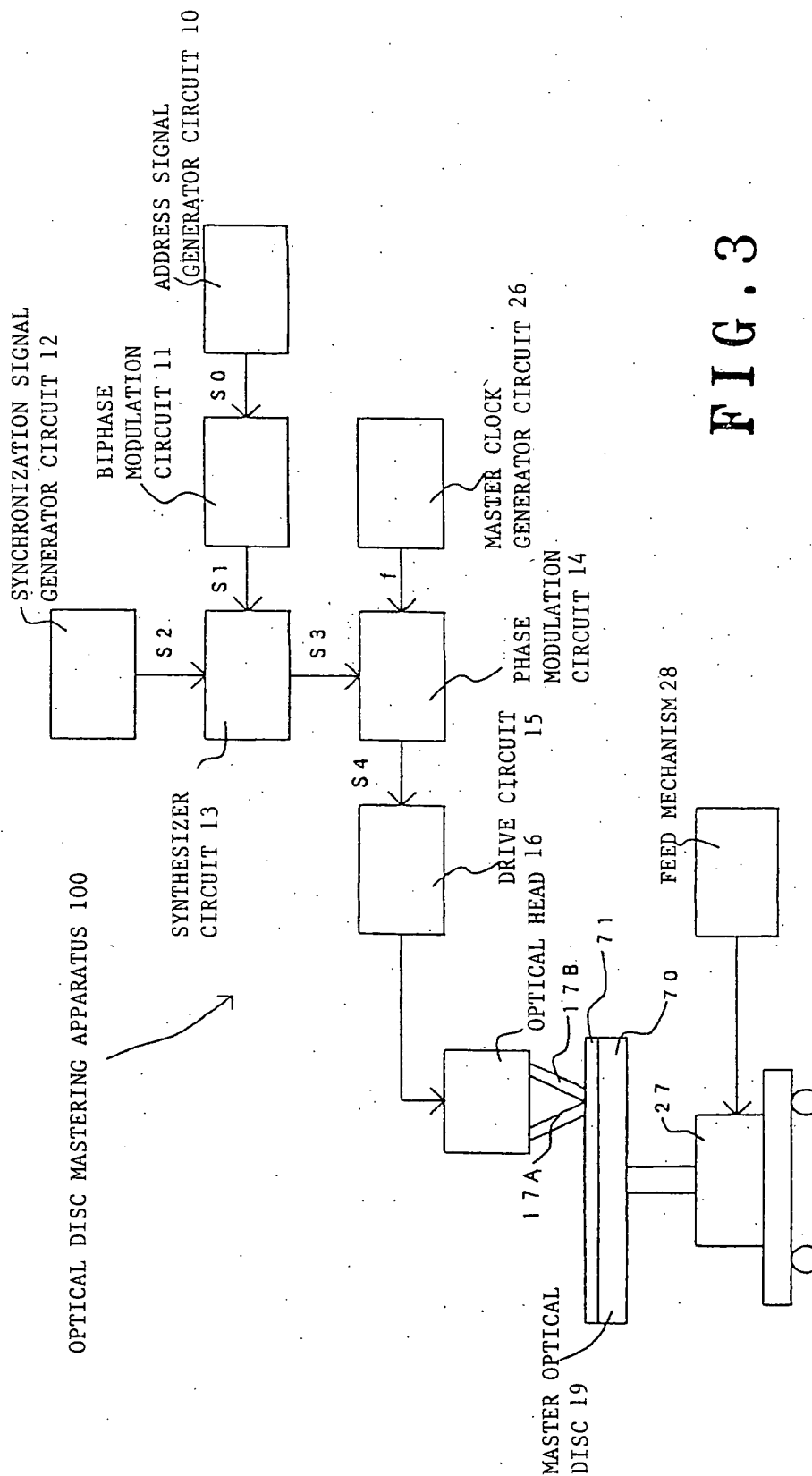


FIG. 3

PU01-0041
~~EP01-0041~~
~~PCN01-0041~~
~~PTW01-0041~~
~~PKR01-0041~~

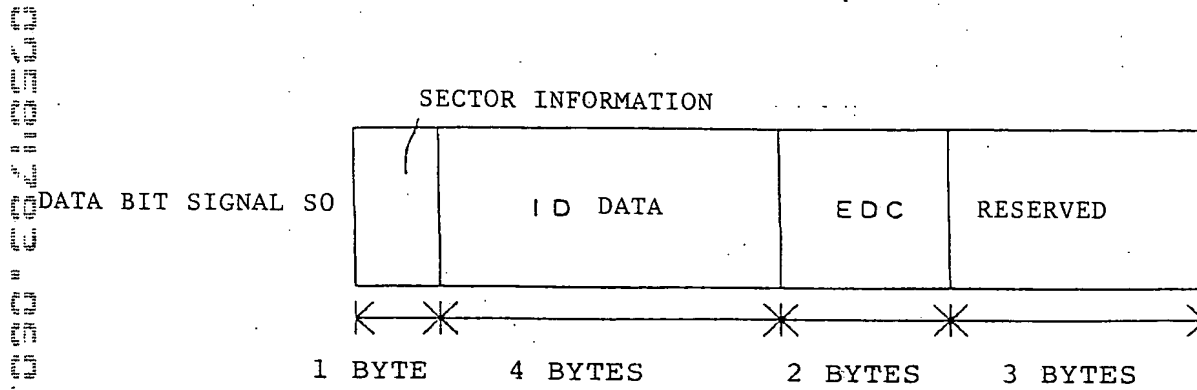


FIG. 4

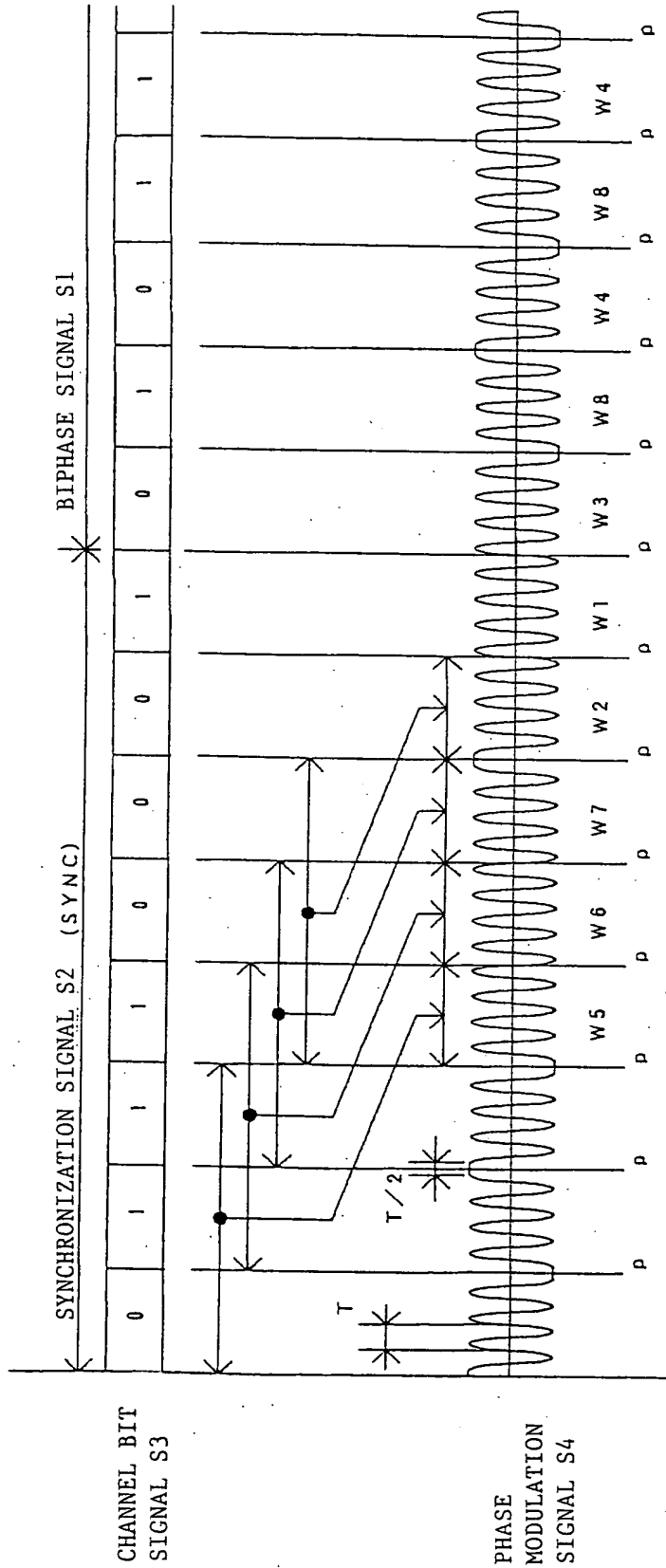


FIG. 5

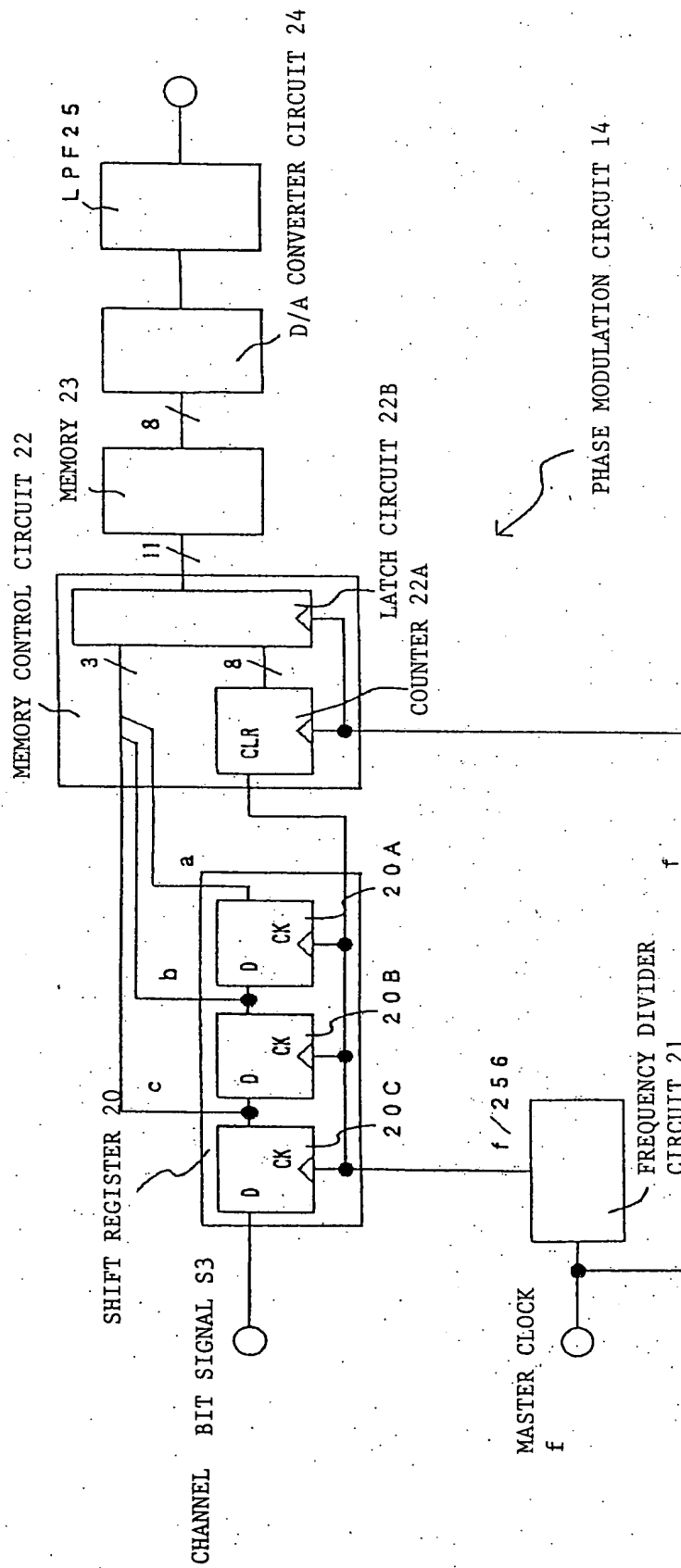
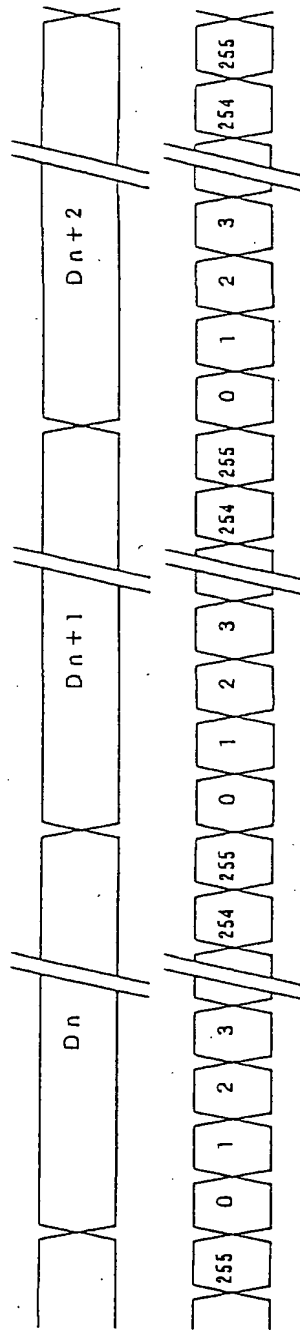


FIG. 6

PU01-0041
~~EP01-0041~~
~~PCN01-0041~~
~~PTW01-0041~~
~~PKR01-0041~~



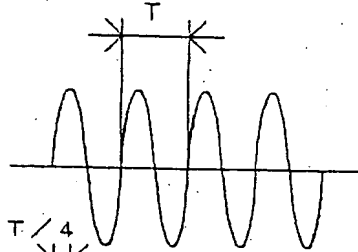
OUTPUT OF SHIFT
 REGISTER 20(3 MOST
 SIGNIFICANT BITS OF
 ADDRESS)

OUTPUT OF COUNTER
 22A(8 LEAST
 SIGNIFICANT BITS OF
 ADDRESS)

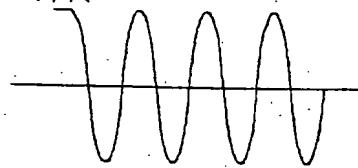
FIG. 7

FIG. 8

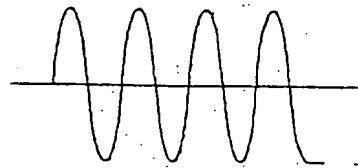
BASIC WAVEFORM W 1
(0 0 0)



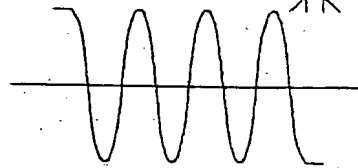
BASIC WAVEFORM W 2
(1 0 0)



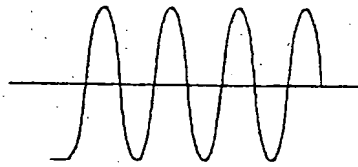
BASIC WAVEFORM W 3
(0 0 1)



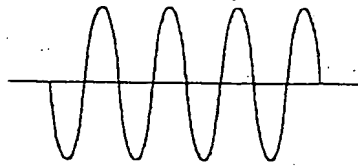
BASIC WAVEFORM W 4
(1 0 1)



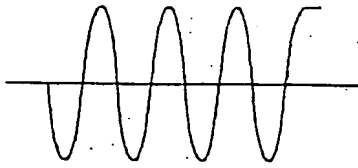
BASIC WAVEFORM W 5
(0 1 1)



BASIC WAVEFORM W 6
(1 1 1)



BASIC WAVEFORM W 7
(1 1 0)



BASIC WAVEFORM W 8
(0 1 0)

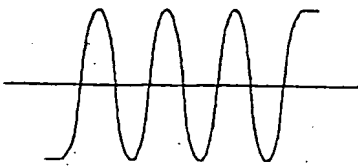
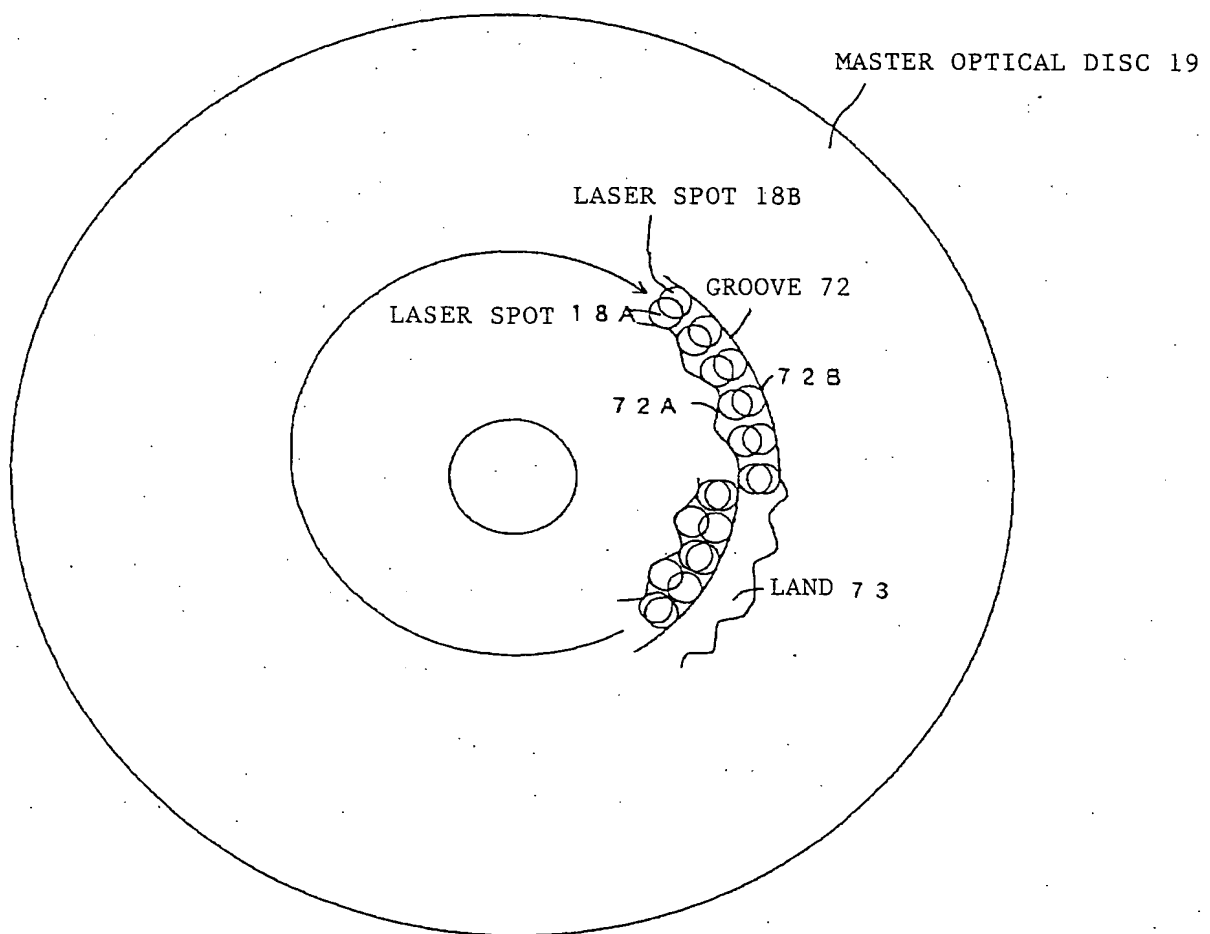
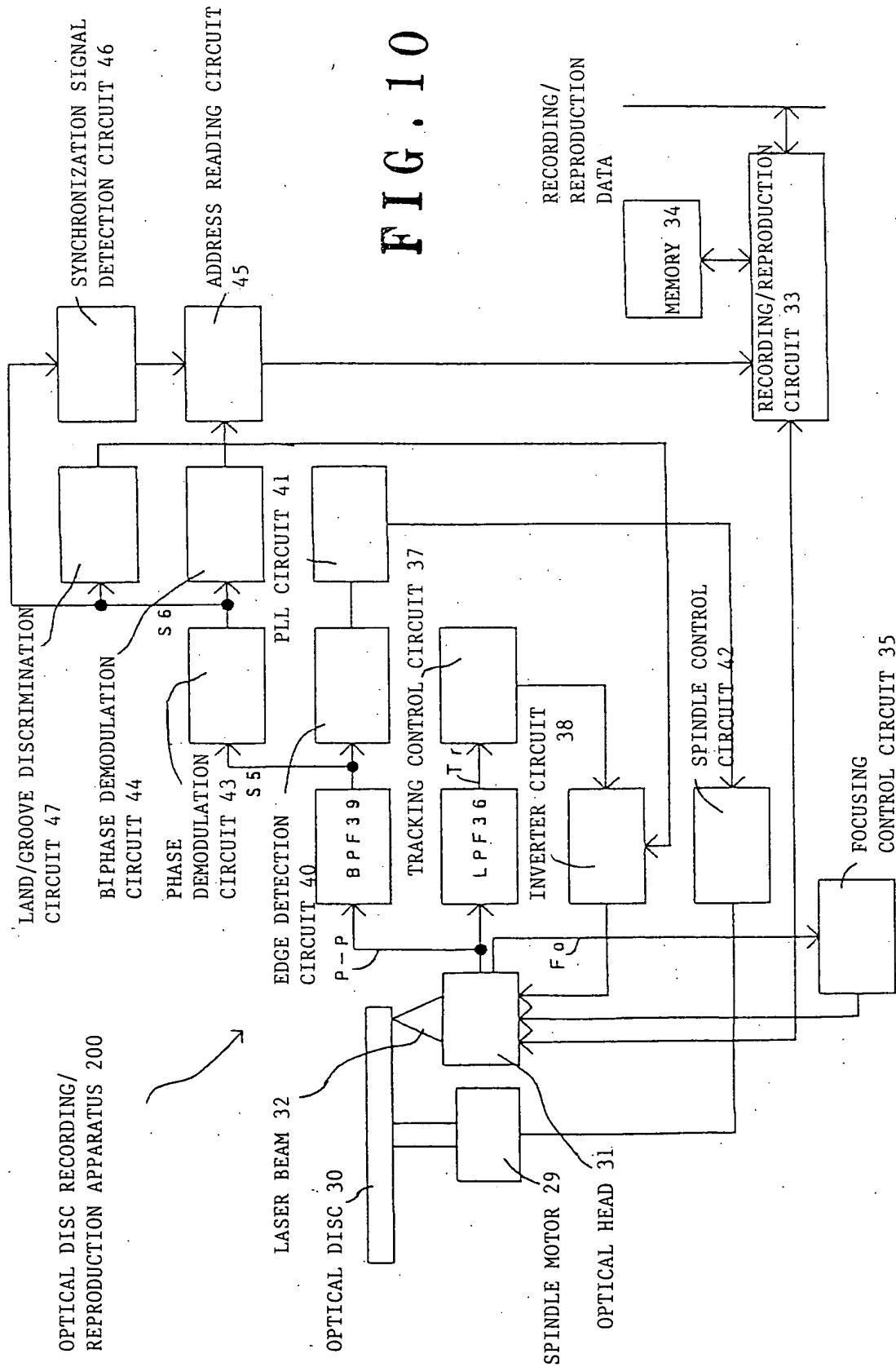


FIG. 9





PU01-0041
~~EP01-0041~~
~~PCN01-0041~~
~~PTW01-0041~~
~~PKR01-0041~~

FIG. 11A

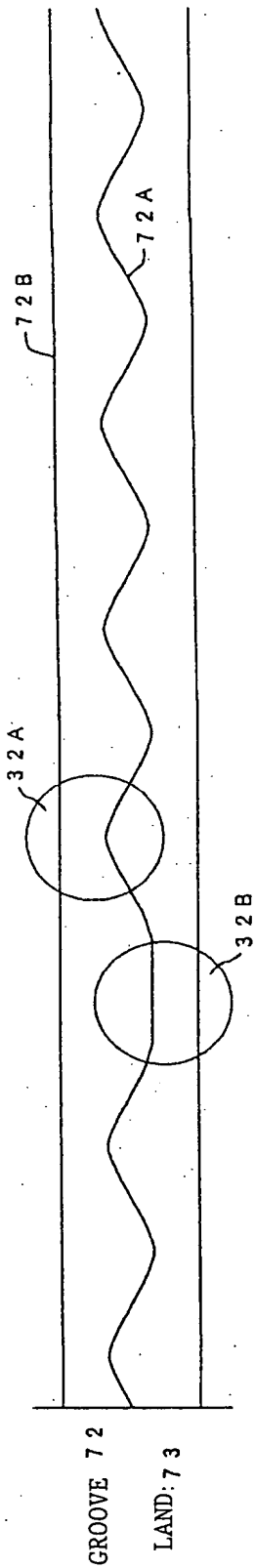


FIG. 11B

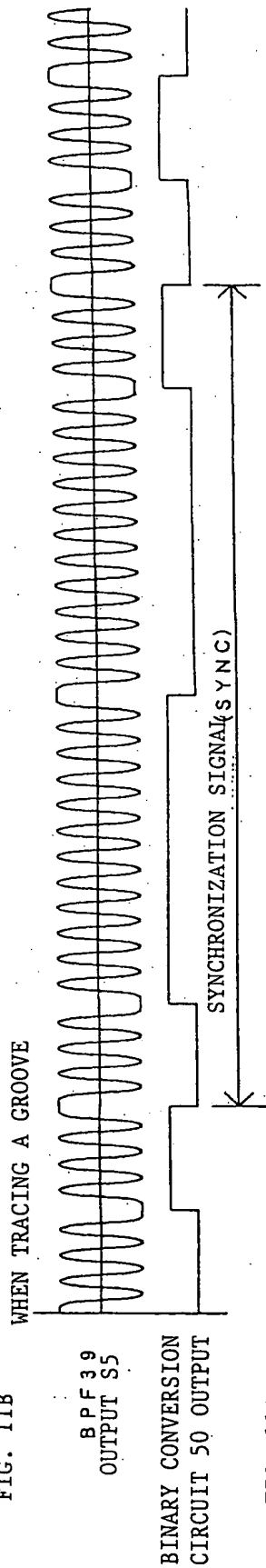


FIG. 11C

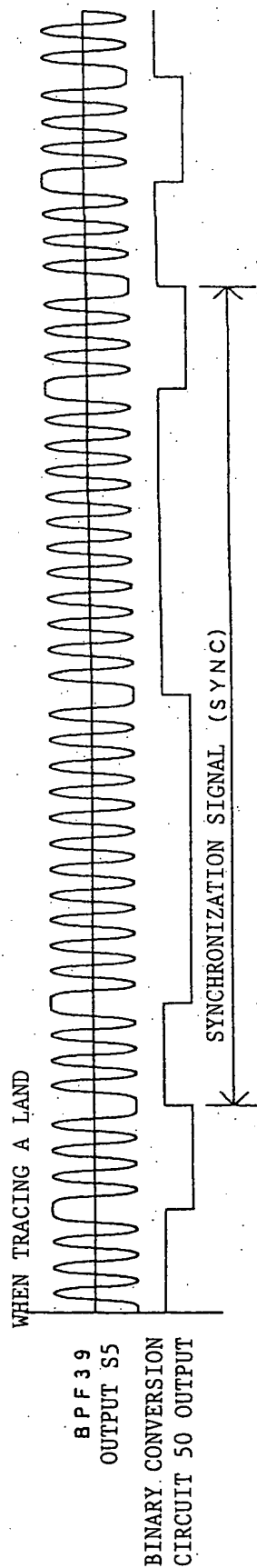


FIG. 12

LAND/GROOVE DISCRIMINATION
CIRCUIT 47

